

FIGURE 1

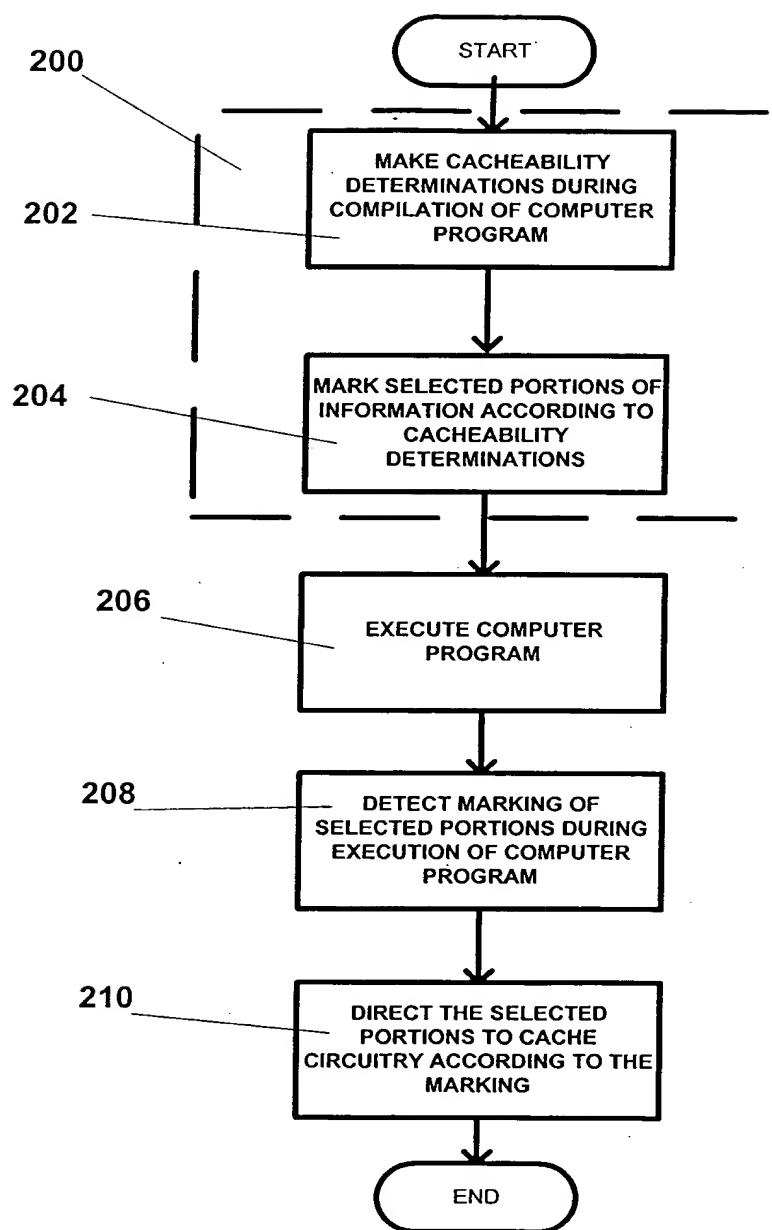


FIGURE 2

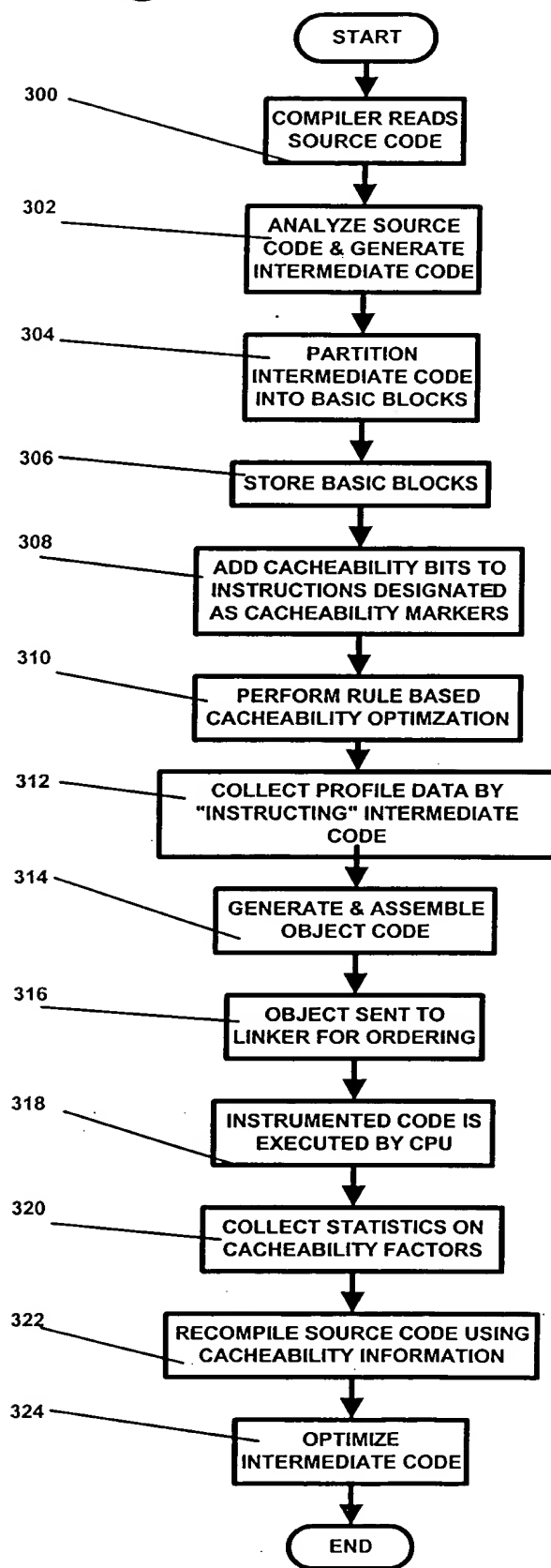


FIGURE 3

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graph TD
    400([START]) --> 401[FETCH INSTRUCTION]
    401 --> 402{IS INSTRUCTION IN CACHE(S) ?}
    402 -- YES --> 404[GET INSTRUCTION FROM CACHE]
    402 -- NO --> 406[GET INSTRUCTION FROM MAIN MEMORY]
    406 --> 408{IS INSTRUCTION CACHEABLE ?}
    408 -- NO --> 412[DELIVER INSTRUCTION TO EXECUTION UNIT]
    408 -- YES --> 410{WHICH CACHE ?}
    410 -- L2 --> 416[STORE INSTRUCTION IN LEVEL 2 CACHE]
    410 -- L1 --> 414[STORE INSTRUCTION IN LEVEL 1 CACHE]
    412 --> 418([CONTINUE])
    416 --> 418
    414 --> 418
    404 --> 418

```

FIGURE 4

056571 050000

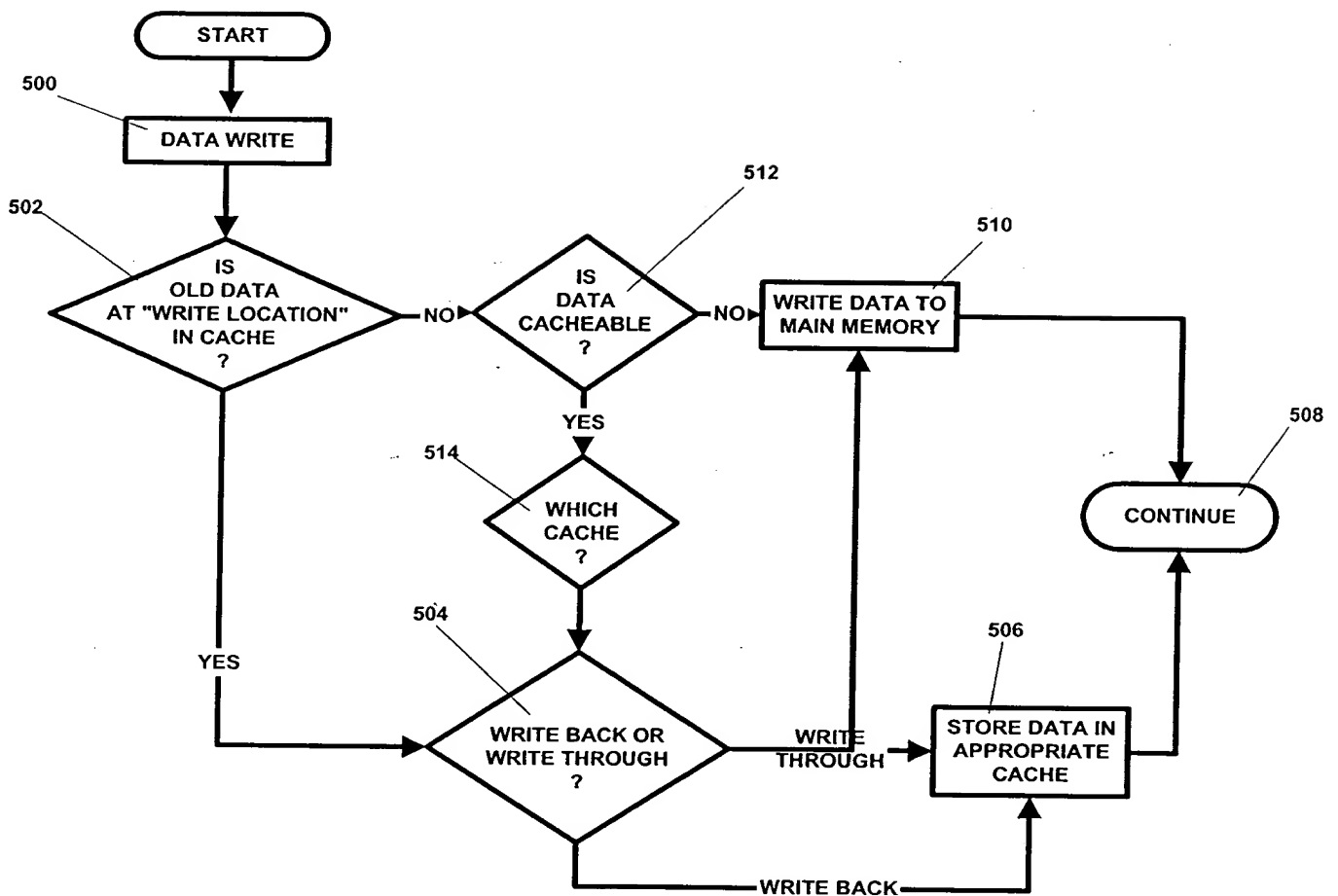


FIGURE 5